

Andrew V. Podlesny et al.
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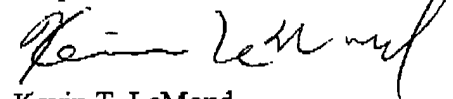
10 an output stage coupled to an output of said first stage;
11 wherein the output of the first stage is coupled to an input of the output
12 stage;
13 wherein the differential bus and the differential data bus are precharged to a
14 voltage V_{pr} between V_{dd} and ground, where $V_{pr} = K \cdot V_{dd}$, and K is a precharging voltage factor.

CONCLUSION

In view of the foregoing, Applicants believe all claims now pending in this Application are in condition for allowance. The issuance of a formal Notice of Allowance at an early date is respectfully requested.

If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at 415-576-0200.

Respectfully submitted,



Kevin T. LeMond
Reg. No. 35,933

TOWNSEND and TOWNSEND and CREW LLP
Two Embarcadero Center, 8th Floor
San Francisco, California 94111-3834
Tel: (415) 576-0200
Fax: (415) 576-0300
KTL:lo
SF 1258563 v1